ABSTRACT OF THE DISCLOSURE

An image processing apparatus includes a CPU. In a camera mode, a TIFF file in which main image data is stored in such a manner as to satisfy a predetermined condition is recorded by the CPU in a recording medium. When such the TIFF file is selected in a reproducing mode, the TIFF file itself is transferred from the recording medium to an SDRAM, and the main image data included in this TIFF file is subjected to a reproducing process by an ASIC. Herein, the predetermined condition includes a condition that a deviance amount between a head location of the TIFF file and a head location of the main image data is an integral multiple of "4", that is, the number of bytes storable in one address of the SDRAM.